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	LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	
Applicant: Andrew Graham et al.		
Filing Date: Herewith Priority Date: Oct. 31, 2002 Int'l Filing Date: Oct. 29, 2003		Group Art: 2818

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
/AL/	5,106,778	4/1992	Hollis et al.			
/AL/	5,612,563	3/1997	Fitch et al.			
/AL/	2002/0001905	1/2002	Choi et al.			

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translated Yes No
/AL/	100 36 897	1/2002	DE			No (Equivalent included)
↓	0 452 950	10/1991	EP			Yes
	WO 01/57917	8/2001	PCT			Yes
	WO 01/61753	8/2001	PCT			No (Equivalent included)
↓	WO 03/026034	3/2003	PCT			No (Translated Abstract)

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/AL/	Choi, Won Bong et al., "Ultrahigh-Density Nanotransistors by Using Selective Grown Vertical Carbon Nanotubes," American Institute of Physics, Applied Physics Letters, Vol. 79, No. 22, pp. 3696-3698, (November 26, 2001).
↓	Derycke, V. et al, "Carbon Nanotube Inter- and Intramolecular Logic Gates," American Chemical Society, Nano Letters, Vol. 1, No. 9, pp. 453-456, (November 26, 2001).
	Harris, Peter J. F., "Carbon Nanotubes and Related Structures," Cambridge University Press, pp. 1-15 and 111-155 (1999).
	Huang, Yu et al., "Logic Gates and Computation from Assembled Nanowire Building Blocks," Science, Vol. 294, pp. 1313-1317, (November 9, 2001).
↓	Johnson, Justin C. et al., "Single Nanowire Lasers," J. Phys. Chem. B, pp. Est. 3-6, (November 26, 2001).

EXAMINER: /Andres Lopez Esquerra/

DATE CONSIDERED: 04/12/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.